IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. 10/618,440

Confirmation No. 4206

Inventor(s): Rohit DUBEY

Title: IMPROVED BOUNDARY SCAN
OF INTEGRATED CIRCUITS

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Examiner: Cvnthia H. BRITT

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Customer No. 25235

TRANSMITTAL OF ISSUE AND PUBLICATION FEES

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The attached Part B—Fee Transmittal authorizes the large entity Issue and Publication Fees and any fee deficiencies associated with this filing to be charged to Deposit Account 50-1123.

Respectfully submitted,

April 9, 2007

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